Examiner Text Search.

DBs Time Stamp Ref Default **Plurals** Hits Search Query Operator # **S26** (board or substrate or pwb or pcb) US-PGPUB; OR OFF 2005/06/06 18:59 614 with (vdd) with (and or ground) USPAT; USOCR; EPO; JPO; DERWENT; IBM\_TDB **S27** OFF 126 S26 and (high adj voltage) **US-PGPUB:** OR 2005/06/06 18:59 USPAT; USOCR; EPO; JPO; DERWENT; IBM\_TDB S28 OFF 125 S27 not (S24 or S17 or S15 or S13 US-PGPUB; OR 2005/06/06 19:01 or S12 or S11 or S10 or S9 or S8 or USPAT; S7 or S4 or S3) USOCR; EPO; JPO; DERWENT; IBM\_TDB S29 475 S26 not (S28 or S24 or S17 or S15 US-PGPUB; OR OFF 2005/06/06 19:06 or S13 or S12 or S11 or S10 or S9 USPAT; or S8 or S7 or S4 or S3) USOCR; EPO; JPO; DERWENT; IBM\_TDB S30 21 (vdd adj wiring) with (gnd adj US-PGPUB; OR OFF 2005/06/06 19:11 wiring) USPAT; USOCR; EPO; JPO; DERWENT; IBM\_TDB S33 (pwb or pcb or wiring adj board or US-PGPUB; OFF OR 2005/06/08 14:35 cirucit adj board) with (split adj USPAT; plane\$1) USOCR; EPO; JPO; DERWENT; IBM\_TDB S34 (pwb or pcb or wiring adj board or US-PGPUB; OR **OFF** 2005/06/08 11:58 cirucit adj board) same (split adj USPAT; plane\$1) USOCR; EPO; JPO; DERWENT; IBM\_TDB S35 (pwb or pcb or wiring adj board or US-PGPUB; 11 OR OFF 2005/06/08 12:07 cirucit adj board) and (split adj USPAT; plane\$1) USOCR; EPO; JPO; DERWENT; IBM\_TDB

Application # 10/676,732

S36	5	S35 not (S34 or S33)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/08 11:56
S37	19	(pwb or pcb or wiring adj board or cirucit adj board) with (split with plane\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/08 11:56
S38	14	S37 not (S35 or S34 or S33)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/08 11:59
S39	26	(pwb or pcb or wiring adj board or cirucit adj board) same (split with plane\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/08 11:59
S40	5	S39 not (S37 or S35 or S34 or S33)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/08 12:07
S41	109	(pwb or pcb or wiring adj board or cirucit adj board) and (split with plane\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/08 12:07
S42	79	S41 not (S39 or S37 or S35 or S34 or S33)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/08 12:07
S46	7	(pwb or pcb or wiring adj board or cirucit adj board) with (power adj distribution adj plane\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/08 14:45

S47	6	(pwb or pcb or wiring adj board or cirucit adj board) and (trace\$1 or pattern\$1 or strip\$1) and (residual adj area\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/08 14:46
S48	11	(pwb or pcb or wiring adj board or circuit adj board) and (trace\$1 or pattern\$1 or strip\$1) and (residual adj area\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/08 14:47
S61	7680	(174/250-268).CCLS.	US-PGPUB; USPAT	OR	OFF	2005/06/08 17:33
S62	1353	(361/792-795).CCLS.	US-PGPUB; USPAT	OR	OFF	2005/06/08 17:33
S63	8211	S61 or S62	US-PGPUB; USPAT	OR	OFF	2005/06/08 17:34
S64	399	S63 and (power adj plane)	US-PGPUB; USPAT	OR	OFF	2005/06/08 17:34